

**GOVERNMENT OF INDIA
COMMUNICATIONS AND INFORMATION TECHNOLOGY
LOK SABHA**

UNSTARRED QUESTION NO:3680
ANSWERED ON:18.03.2015
MICRO ELECTRONICS AND NANO TECHNOLOGY
Kalvakuntla Smt. Kavitha

Will the Minister of COMMUNICATIONS AND INFORMATION TECHNOLOGY be pleased to state:

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- (a) whether the Government has launched micro electronics and nano-technology development programme;
- (b) if so, the details thereof and the number of companies including in private sector that are working in this field as on date;
- (c) the funds allocated and utilized by the Government under the programme; and
- (d) the details of incentives being provided to the sector?

Answer

MINISTER FOR COMMUNICATIONS AND INFORMATION TECHNOLOGY
(SHRI RAVI SHANKAR PRASAD)

(a): Yes, Sir.

(b): 1. DeitY's Microelectronics & Nanotechnology Development Program is working towards promotion of R&D in microelectronics & nanotechnology area. The broad objective of the Microelectronics and Nanotechnology Development Programme is to establish Nanoelectronics & Microelectronics base in the country through setting up of Centers of Excellence, technology development & capacity building through sponsored R&D projects in the areas of Nanoelectronics, Microelectronics, Micro and Nano -Electro Mechanical Structures (MEMS & NEMS), Nanometrology, Design (including fabrication), characterization, testing and modeling of digital, analog & mixed signal VLSI/ application specific integrated circuits (ASIC) etc. Further details regarding Microelectronics and Nanotechnology R&D projects may be obtained from the DeitY web-site <http://deity.gov.in/content/microelectronics-development-division> and <http://deity.gov.in/content/nanotechnology-initiative-division> respectively.

2. The Government does not maintain list of companies working in the field.

(c): The details of funds allocated for research & development under the Microelectronics & Nanotechnology Development programme and utilized by the Government are as follows:

Rs. in crores

Financial year	Funds allocated	Actual expenditure
2011-2012	92.50	90.17
2012-2013	60.00	37.75
2013-2014	56.84	56.84
2014-15	75.00	68.29 (till date)

(d): Various incentives are provided to companies in the area of Microelectronics and Nanotechnology. The major incentives include:

- 1. Modified Special Incentive Package Scheme (M-SIPS) to offset disability and attract investments
- 2. Support for International Patents in Electronics & IT (SIP-EIT);
- 3. Scheme for Technology Incubation and Development of Entrepreneurs (TIDE).

In addition, incentives are also available from State Governments