

GOVERNMENT OF INDIA
MINISTRY OF ELECTRONICS AND INFORMATION TECHNOLOGY
LOK SABHA
STARRED QUESTION NO. *28
TO BE ANSWERED ON 24.07.2024

PRODUCTION OF HIGH PURITY SILICON

***28. THIRU ARUN NEHRU:**

Will the Minister of ELECTRONICS AND INFORMATION TECHNOLOGY be pleased to state:

(a) whether the Government has taken any action for indigenous production of high purity silicon keeping in view that India is fast becoming an electronic assembly hub with large factories being setup in Tamil Nadu and the main raw material, silicon is currently being imported and is a weak link on Industrial and national security grounds;

(b) if so, the details thereof;

(c) whether any one has applied for indigenous production under the PLI scheme; and

(d) if so, the details thereof?

ANSWER

MINISTER OF ELECTRONICS AND INFORMATION TECHNOLOGY
(SHRI ASHWINI VAISHNAW)

(a) to (d): A Statement is laid on the Table of the House.

**STATEMENT REFERRED TO IN THE REPLY TO LOK SABHA
STARRED QUESTION NO*28 FOR 24.07.2024 REGARDING
PRODUCTION OF HIGH PURITY SILICON**

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(a) to (d): Under the aegis of National Policy on Electronic (NPE 2019), Government has formulated Scheme for Promotion of manufacturing of Electronic Components and Semiconductors ('SPECS'). Among many other eligible electronic goods, the SPECS scheme also covers for manufacturing of Semiconductor wafers, Solar Photovoltaic (SPV) wafers and Solar Photovoltaic (SPV) Polysilicon which are made from high purity silicon. The scheme provides a financial incentive of 25% on the eligible capital expenditure. The scheme was notified on 01.04.2020 and was open to receive applications upto 31.03.2024. Under the scheme, so far 49 applications with project cost of Rs. 14,120.66 crore have been approved.
